

Amendments to the Specification:

Please replace the paragraph beginning with “An I2C transceiver circuit 36 is” from page 5, line 12, with the following paragraph:

An I2C transceiver circuit 36 is implemented in the tuner 12. I2C transceiver 36 provides a voltage to voltage input 26 to control the switch circuit 10. Transceiver 36 receives commands via an I2C bus and controls switch 10 accordingly, switching between a first state, where a signal path is established from first input terminal 20 to output terminal 24 and a second state where a signal path is provided from second input terminal 22 to output terminal 24.

Please replace the paragraph beginning with “I2C transceiver 36 provides” from page 5, line 18, with the following paragraph:

I2C transceiver 36 provides a voltage signal RFSW, which is fed into control circuit 34. Control circuit 34 is a complementary driver circuit producing an in-phase voltage V_{SW} to drive second SPST switch 30 and an inverted voltage \overline{V}_{SW} to drive first SPST switch 28. Control circuit 34 further provides a driver for switch signal PESW to drive the SPDT switch 32.